IN THE CLAIMS

Please amend claims as follows:

1. (Currently amended) A distributed base station system, comprising:

a first base band unit (BBU) in communication with a Radio Frequency unit (RFU);[[,]] ~~configured to comprise:~~

wherein the first BBU comprises:

~~a Main Processing & Timing unit which comprises~~

a main processing unit ~~and~~;

a clock unit;

a base band signal processing unit;

a transmission unit; and

an interface unit~~, configured to:~~;

wherein the interface unit is configured to:

~~intercommunicate data with an external unit;~~

intercommunicate digital base band signals with the base band signal processing unit; and

intercommunicate master control information with the main processing unit;

wherein the interface unit comprises a primary base band Radio Frequency (RF) interface; ~~and the interface unit is integrated with the Main Processing & Timing unit, the base band signal processing unit and the transmission unit; and~~

~~a Radio Frequency unit (RFU) ,~~

wherein the RFU comprises a secondary base band RF interface and the RFU is connected to the primary base band RF interface of the BBU;[[.]]

wherein the distributed base station system further comprises at least a second BBU;

and

wherein the interface unit of the first BBU further comprises a capacity expansion interface, configured to communicate with the second BBU.

2. (Original) The system according to Claim 1, wherein the primary base band RF interface and the secondary base band RF interface both are high speed digital interfaces.

3. (Currently amended) The system according to Claim 1, wherein ~~the base station system comprises a plurality of BBUs, and the BBUs are interconnected with each other via wire cables or optical fibers; the interface unit of each BBU comprises: a~~ the ~~primary~~ capacity expansion interface[[,]] is further configured to transmit ~~synchronous clock signals, base band information, transmission information and~~ the master control information or the base band signals ~~among BBUs, and to achieve interconnection and data sharing among BBUs~~ from the first BBU to the second BBU.

4. (Currently amended) The system according to Claim 3, wherein the first BBU connects with the second BBU, and wherein the ~~primary~~ capacity expansion interface comprises a primary capacity expansion interface that provides an active/standby switchover control signal.

5. (Original) The system according to Claim 3, wherein the interface unit further comprises an identification interface for marking the type of the base station and the position of the BBU.

6. (Original) The system according to Claim 3, wherein the interface unit further comprises a Dry Contact input interface for expanding the input Dry Contact functions of the base station.

7. (Currently amended) The system according to Claim 3, wherein the first ~~BBUs~~ BBU and the second BBU comprise: a master BBU that works in an active state and a standby BBU that works in a standby state.

8. (Canceled)

9. (Currently amended) The system according to Claim [[8]] 3, wherein the RFU is connected with any one of the first ~~plurality of BBUs~~ BBU and the second BBU.

10. (Currently amended) The system according to Claim [[7]] 1, wherein the ~~BBUs~~ first BBU and second BBU comprise a slave BBU that works in a slave state.

11. (Currently amended) The system according to Claim [[3]] 1, further comprising:
an exchange base band (BB) cassette with a plurality of ~~secondary~~ capacity expansion interfaces[[,]] ; ~~and~~ wherein ~~each~~ the first BBU is connected with ~~one of the~~ a ~~secondary~~ capacity expansion interface ~~interfaces~~ ~~on~~ of the exchange BB cassette via the ~~respective primary~~ capacity expansion interface of the first BBU; and

wherein the second BBU is connected with another capacity expansion interface of the exchange BB cassette.

12-38. (Canceled)

39. (Currently amended) A base band unit (BBU), comprising:

~~a Main Processing & Timing unit which comprises~~

a main processing unit ~~and~~;

a clock unit~~, configured to control a base station, exchange signals and traffic data among the units in the base station and provide clock signals~~;

a base band signal processing unit~~, configured to process symbol level and chip level digital signals in physical layer~~;

a transmission unit ~~which is connected with a base station controller, configured to intercommunicate data information between the base station and the base station controller~~; and

an interface unit~~, configured to~~;

wherein the interface unit is configured to

~~intercommunicate with external data information;~~

intercommunicate digital base band signals with the base band signal processing unit; and

intercommunicate master control information with the main processing unit;

wherein the interface unit comprises a primary base band RF interface ~~and the interface unit is integrated with the Main Processing & Timing unit, the base band signal processing unit, and the transmission unit~~ configured to connect with a Radio Frequency unit (RFU);

wherein the interface unit of the BBU further comprises a capacity expansion interface, configured to communicate with another BBU.

40. (Currently amended) The ~~Base~~ base band unit according to Claim 39, wherein the primary base band RF interface is a high speed digital interface.

41. (Canceled)

42. (Currently amended) The ~~Base~~ base band unit according to Claim 39, wherein the interface unit further comprises an identification interface for marking the type of ~~the~~ a base station in which the BBU belongs and the position of the BBU~~, and the identification interface is a DIP switch and /or a cable identification interface~~.

43-44. (Canceled)

45. (Currently amended) The ~~Base~~ base band unit according to Claim 39, wherein the interface unit is further ~~comprises: a capacity expansion interface,~~ configured to transmit the ~~clock synchronous signals, base band information, transmission information and~~ master control information or the base band signals ~~among BBUs, and to achieve interconnection and data sharing among BBUs~~ from the BBU to the other BBU.

46. (Currently amended) The ~~Base~~ base band unit according to Claim 45, wherein the interface unit further comprises at least one of:

a reset interface for resetting ~~the~~ a base station in which the BBU belongs;

an identification interface for marking the type of the base station and the position of the BBU;

a power supply switches for controlling power on and power off for ~~itself~~ the base station;

a test interface for connecting the BBU with an external test equipment ~~equipments~~;

a signal input interface for receiving external clock signals;

a Dry Contact input interface for expanding input Dry Contact functions of the base station;

an electrostatic discharge (ESD) connector; and

a protect ground (PGND) terminal.

47. (Currently amended) The ~~Base~~ base band unit according to Claim 45, wherein the capacity expansion interface comprises one or a plurality of capacity expansion interfaces interface(s) for providing ~~the~~ an active/standby switchover control signal.

48. (Currently amended) The ~~Base~~ base band unit according to Claim 46, wherein the signal input interface comprises ~~at least one of~~ a signal input interface for receiving GPS synchronous clock signals and/or a signal input interface for receiving 2M synchronous clock signals.

49. (Currently amended) The ~~Base~~ base band unit according to Claim 46, wherein the test interface comprises at least one of a 10M test interface for outputting 10M test synchronous clock signals and a transmission time interval (TTI) test interface for outputting TTI signals.

50. (Canceled)

51. (Currently amended) The ~~Base~~ base band unit according Claim 39, wherein the main processing unit ~~Main Processing & Timing unit~~, the clock unit, the base band signal processing unit, the transmission unit and the interface unit are integrated in a BBU cassette ~~on a single board~~.

52. (New) The system according to Claim 1, wherein the main processing unit, the clock unit, the base band signal processing unit, the transmission unit and the interface unit are integrated in a BBU cassette.